

**Amendments to the Specification:**

Please replace paragraph [0015] with the following amended paragraph:

- Step 220: read the logical address of each block in the this zone, and  
5 store the addresses in the memory 24; and

Please replace paragraph [0016] with the following amended paragraph:

- Step 230: determine if the address of each block in the this zone have  
10 been read, if so, the look up table is constructed by the logical address  
stored in each block, if not, repeat step 220.

Please replace paragraph [0020] with the following amended paragraph:

- 15 According to the claimed invention, a method of accessing a nonvolatile  
memory is disclosed. The nonvolatile memory is installed in an  
electronic device and comprises a plurality of zones, each zone  
comprising a plurality of spare blocks, the method comprises the  
following steps: (a) store the look up table of each zone in at least a  
20 spare block which belongs to this zone; (b) the electronic device  
calculates one of the zones of the nonvolatile memory corresponding to a  
logic address used by a host for accessing the nonvolatile memory; (c)  
the electronic device reads the look up table of the zone calculated in step  
(b) from at least one of the spare blocks and stores the look up table in a  
25 memory of the electronic device; and (d) the host accesses the  
nonvolatile memory according to the look up table stored in the memory  
of the electronic device.

Please replace paragraph [0027] with the following amended paragraph:

- 30 In view of the disadvantages of the conventional methods of accessing a  
nonvolatile memory, the present invention provides a method of

accessing a flash memory in which the host constructs look up tables of all zones of the flash memory in at least a spare block when the host reads the flash memory at the first time. Since the flash memory is nonvolatile, the look up tables will be kept in the spare blocks even

5 when power is interrupted. As a result, whenever the host 10 attempts to access the flash memory 30 or changes the zones accessed in the flash memory 30, the controller 22 of the electronic device 20 only needs to read the look up table in the spare block of the flash memory 30 and store the look up tables in the memory 30 so that the time of

10 constructing a new look up table is saved. In addition, since each zone of the flash memory 30 has spare blocks, the look up table of each zone can be correspondingly stored in the spare block of the corresponding zone. ~~Alternatively, the look up tables of all zones can also be stored in the spare blocks of one certain zone.~~ According to the present invention,

15 the controller 22 only has to construct the look up tables once and store the look up tables in the spare blocks, and then read the look up tables stored in the spare blocks of the flash memory 30 whenever the host 10 changes the zone accessed in the flash memory 30. Consequently, the speed of accessing the flash memory 30 is improved. Due to host 10 writing data, the look up table will be updated. We use a new spare block to store the new look up table and erase the old one spare block.

20 Generally speaking, the look up table will not be stored in a fixed physical block. The block which stores look up table could be any unused physical block in this zone.

25

Please replace paragraph [0030] with the following amended paragraph:

Step 320: the controller 22 of the electronic device 20 reads the logical address of each block of each zone to construct the look up tables in the

30 memory 24, and stores the look up tables in the spare block of each corresponding zone or in the spare blocks of one certain zone until all the look up tables are constructed and stored in the spare blocks;

Please replace paragraph [0033] with the following amended paragraph:

- Step 350: the flash memory 30 reads the look up table of the zone  
5 corresponding to the logic address, and updates the look up table in the  
memory 24 into flash memory 30; and

Please replace paragraph [0035] with the following amended paragraph:

- 10 Please refer to Fig.5. Fig.5 is a schematic diagram illustrating how the host 10 accesses the flash memory 30. As shown in Fig.5, a 64MB flash memory is used as an example. The flash memory 30 is divided into four zones of 16MB. Each zone has 1024 blocks where 1000 blocks are used as data blocks and the rest 24 blocks are spare blocks. The capacity of  
15 each block is 16KB, and the look up table corresponding to each zone is stored in the spare blocks. Generally, the capacity of a look up table is 2KB, and the look up tables of each zone can be stored in the spare blocks of the corresponding zone, ~~or in the spare blocks of a certain zone. Any physical block which does not store data can be a spare block~~  
20 to store look up table. If old data is stored in a first block 34 (data block) of zone 1 of the flash memory 30, the host 10 can read the look up table of zone 1 according to the logic address of the old data, and read the old data from the first block 34 according to the look up table of zone 1 for editing the old data. The new data that has been edited is supposed to  
25 replace the old data, however, the flash memory 30 can only be written from “1” to “0” rather than from “0” to “1”. Consequently, a second block 36 (spare block) is therefore used to store new data, and the first block 34 is then erased as a new spare block. Following that the look up table of zone 1 is updated for designating the logic address of the old  
30 data to the second block 36. The address of the look up table is changed after the host 10 accesses the flash memory 30, thus each zone requires a corresponding look up table. Normally, it takes hundreds of

microseconds to construct a look up table of a zone. Nevertheless, it only takes tens of microseconds to read a look up table constructed in the spare block in advance.